



15992 U.S. PTO

120203

Atty. Dkt. No. 039153-0697

00727 U.S. PTO  
10/726472



120203

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Haihong WANG et al

Title: STRAINED SILICON MOSFET  
HAVING SILICON  
SOURCE/DRAIN REGIONS AND  
METHOD FOR ITS FABRICATION

Prior Appl. No.: 10/282,538

Prior Appl. Filing  
Date: October 29, 2002

Examiner: Tuan N. Quach

Art Unit: 2814

**CONTINUING PATENT APPLICATION**  
**TRANSMITTAL LETTER**

Mail Stop PATENT APPLICATION  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Transmitted herewith for filing under 37 C.F.R. § 1.53(b) is a:

☐ Continuation ☒ Division ☐ Continuation-In-Part (CIP)

of the above-identified copending prior application in which no patenting, abandonment, or termination of proceedings has occurred. Priority to the above-identified prior application is hereby claimed under 35 U.S.C. § 120 for this continuing application. The entire disclosure of the above-identified prior application is considered as being part of the disclosure of the accompanying continuing application and is hereby incorporated by reference therein.

☐ Applicant claims small entity status under 37 CFR 1.27.

Enclosed are:

- ☒ Specification, Claim(s), and Abstract (15 pages).
- ☒ Formal drawings (6 sheets, Figures 1-4).
- ☒ Copy of Declaration and Power of Attorney (3 pages).

- ☐ Assignment of the invention to Advanced Mirco Devices, Inc..
- ☐ Assignment Recordation Cover Sheet.
- ☐ Small Entity statement.
- ☒ Request for application not to be published with certification under 35 USC 122(b)(2)(B)(i).
- ☐ Information Disclosure Statement.
- ☐ Form PTO-1449 with copies of \_\_\_\_ listed reference(s).
- ☐ Preliminary Amendment.
- ☒ Application Data Sheet (37 CFR 1.76).

The filing fee is calculated below:

	Claims as Filed	Included in Basic Fee	Extra Claims	Rate	Fee Totals
Basic Fee				\$770.00	\$770.00
Total	14	- 20	= 0	x \$18.00	= \$0.00
Claims:					
Independ	2	- 3	= 0	x \$86.00	= \$0.00
ents:					
If any Multiple Dependent Claim(s) present:				+ \$290.00	= \$0.00
				SUBTOTAL:	= \$770.00
<input type="checkbox"/>				Small Entity Fees Apply (subtract ½ of above):	= \$0.00
				TOTAL FILING FEE:	= \$770.00

- ☒ A check in the amount of \$770.00 to cover the filing fee is enclosed.
- ☐ The required filing fees are not enclosed but will be submitted in response to the Notice to File Missing Parts of Application.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

Date December 2, 2003

By 

FOLEY & LARDNER

Customer Number: 23392

Telephone: (310) 975-7964

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Ronald Coslick

Attorney for Applicant

Registration No. 36,489

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 039153-0697

In re patent application of

Haihong WANG et al.

Group Art Unit: Unassigned

Prior Appl. No. 10/282,538

Examiner: Unassigned

Prior Filing Date: October 29, 2002

For: STRAINED SILICON MOSFET HAVING SILICON SOURCE/DRAIN REGIONS AND METHOD FOR ITS FABRICATION

**CERTIFICATE OF MAILING BY EXPRESS MAIL**

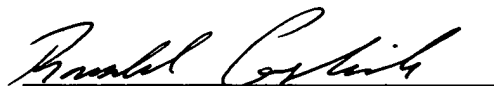
Mail Stop  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Commissioner:

I hereby certify that the following paper(s) and/or fee along with any attachments referred to or identified as being attached or enclosed are being deposited with the United States Postal Service as Express Mail (Express Mail Label No. EV060047053US) under 37 C.F.R. § 1.10 on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

1. Continuing Patent Application Transmittal Letter
2. Application Data Sheet
3. Request and Certification Under 35 USC 122(b)(2)(B)(i)
4. Specification, Claims and Abstract (15 pages)
5. Copy of executed Declaration and Power of Attorney
6. Formal Drawings (6 sheets, Figs. 1-4)
7. Check No. 16596 (\$770) for filing fee
8. Postcard

Respectfully submitted,



Ronald Coslick  
Reg. No. 36,489

December 2, 2003

Date

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MOSFET HAVING SILICON  
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Examiner: Tuan N. Quach

Art Unit: 2814

**REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)**

Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Applicants hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. Applicants hereby request that the attached application not be published under 35 U.S.C. 122(b).

Respectfully submitted,

Date December 2, 2003

By 

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